

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	Implementing Complex Clock Designs in Field Programmable Devices																														
<p>Application Number : Confirmation Number: First Named Applicant: Venkatesh NATARAJAN Attorney Docket Number: TI-36308 Art Unit: Examiner: Search string: (6138266 or 6691287).pn</p>																															
<h3>US Patent Documents</h3> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>6138266</td><td>1998-06-15</td><td>Ganesan et al</td><td></td><td></td><td></td></tr><tr><td></td><td>2</td><td>6691287</td><td>2000-12-14</td><td>Ganesan et al</td><td></td><td></td><td></td></tr></tbody></table>								init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	6138266	1998-06-15	Ganesan et al					2	6691287	2000-12-14	Ganesan et al			
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